2SD1367

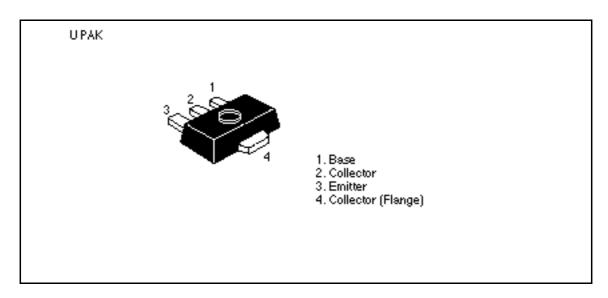
Silicon NPN Epitaxial

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Application

- Low frequency power amplifier
- Complementary pair with 2SB1001

Outline





2SD1367

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol		Unit	
Collector to base voltage	V_{CBO}	20	V	
Collector to emitter voltage	V_{CEO}	16	V	
Emitter to base voltage	V_{EBO}	6	V	
Collector current	I _c	2	А	
Collector peak current	i _{C(peak)} *1	3	А	
Collector power dissipation	P _c *²	1	W	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW 10 ms, Duty cycle 20%.

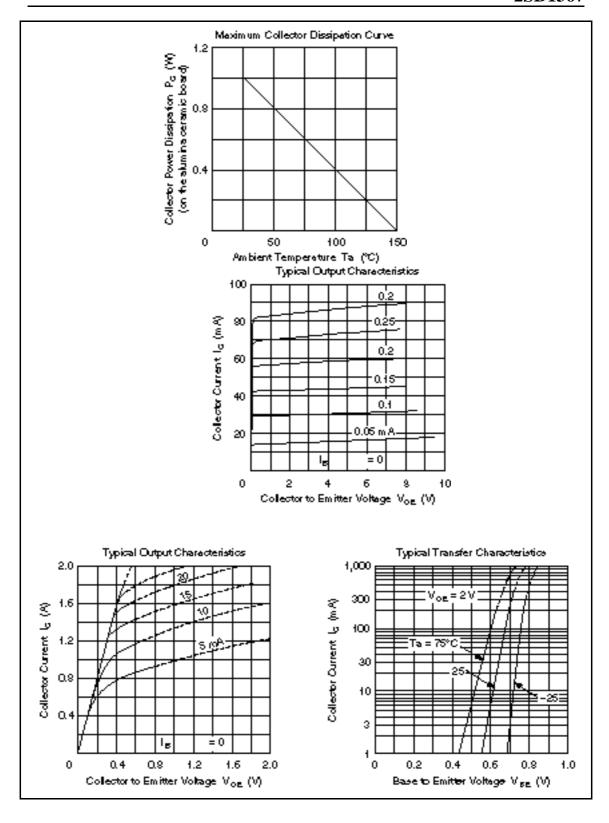
2. Value on the alumina ceramic board (12.5 \times 20 \times 0.7 mm)

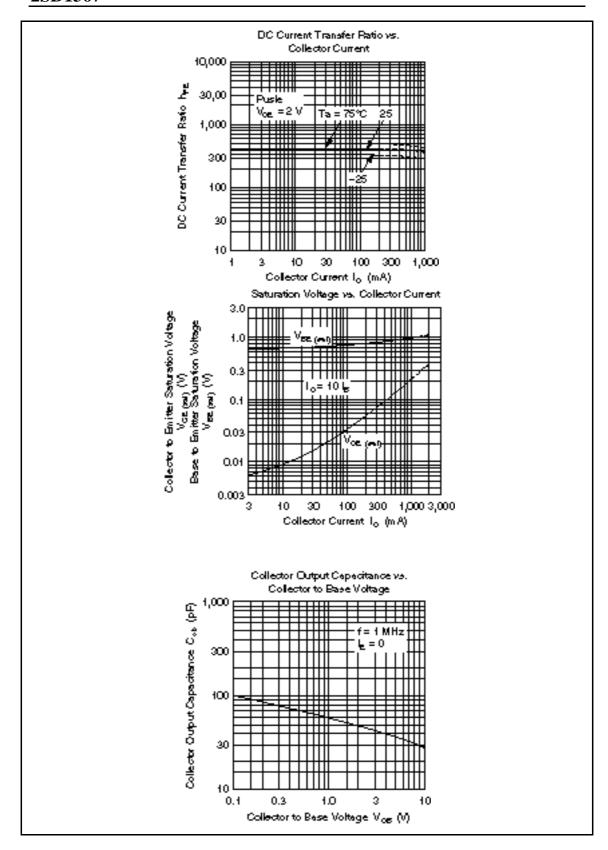
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	20	_	_	V	$I_{c} = 10 \ \mu A, \ I_{e} = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	16	_	_	V	$I_C = 1 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	6	_	_	V	$I_{E} = 10 \ \mu A, \ I_{C} = 0$
Collector cutoff current	I _{CBO}	_	_	0.1	μΑ	$V_{CB} = 16 \text{ V}, I_{E} = 0$
Emitter cutoff current	I _{EBO}	_		0.1	μΑ	$V_{EB} = 5 \text{ V}, I_{C} = 0$
DC current transfer ratio	h _{FE} *1	100	_	500		$V_{CE} = 2 \text{ V}, I_{C} = 0.1 \text{ A}, \text{ Pulse}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	0.15	0.3	V	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 0.1 \text{ A}, \text{ Pulse}$
Base to emitter saturation voltage	$V_{BE(sat)}$	_	0.9	1.2	V	I _C = 1 A, I _B = 0.1 A, Pulse
Gain bandwidth product	f _T	_	100	_	MHz	$V_{CE} = 2 \text{ V}, I_{C} = 10 \text{ mA}$
Collector output capacitance	Cob	_	20	_	pF	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$

Note: 1. The 2SD1367 is grouped by h_{FE} as follows.

Mark	ВА	BB	ВС
h _{FE}	100 to 200	160 to 320	250 to 500





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